



In re the Patent Application of

Ichiro FUJIWARA

Serial No.: 09/431,076

Filed: November 1,1999

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND PROCESS OF PRODUCTION AND WRITE METHOD THEREOF

2813 Art Unit:

J. Chen Examiner:

RESPONSE ELECTING INVENTION

Assistant Commissioner of Patents Washington, DC 20231

In full and timely response to the Official Action of sir: February 6, 2001 (Paper No. 06), the Applicant, through its representatives and attorneys, hereby elects the invention of Group II, having claims 1 to 22, apparently drawn to a semiconductor device, classified in class 257, subclass 300+. Please hold the claims 13 to 27 and 28 to 51, for Groups I and III respectively, in abeyance pending a filing of divisional applications as of right.

The Applicant preserves its right under MPEP §821.04 to rejoin withdrawn process of making and/or process of using claims when elected product claims are found to be allowable.

The Office Action was said to be " $\{r\}$ esponsive to communication(s) filed on Nov 1, 1999", without acknowledgement of the Preliminary Amendment filed on May 2, 2000.

SON-1690

PATENT APPLICATION

An early examination on the merits of the elected claims is respectfully requested.

A. 250

Respectfully submitted,

em Tom 41,518 Ronald P. Kananen
For Reg. No. 24,104

Date: March 1, 2001

RADER, FISHMAN & GRAUER, PLLC

The Lion Building 1233 20th Street, N.W., Suite 501

Washington, D.C. 20036 Tel: (202) 955-3750 Fax: (202) 955-3751

Customer No. 23353